

# **Diffusion soldering fundamentals and application**

# Conventional soldering



**Soldering**: joining process below 425 °C using filler metals (solders) having melting temperatures below those of base metals

**Solders act by:**

- wetting the metal surfaces forming the joints,
- flowing between these surfaces filling the space between them,
- metallurgically bonding to the surfaces when solidified.

# Conventional soldering

Directive 2002/95/EC - New electrical and electronic equipment should be free of lead and other hazardous substances by July 1st, 2006.

WEEE 2002/96/EC - Waste Electrical and Electronic Equipment (WEEE) – deadline 2008

## **Substitutes for Sn-Pb solders**

Sn-based multicomponent alloys with alloying elements such as Bi, In, Ag and Zn

# Challenges for new joints

## Limitation

**Service temperature: 60-100 °C below soldering temperature (220-260 °C for Sn-Pb solders)**

Operating temperature of many electronic devices built on silicon carbide and III-V compound semiconductors exceeds 350 °C (jet engines, nuclear reactors, geothermal wells, automotive electronics, industrial robots, and space electronics).

Miniaturization of components and contact areas in electronic packaging increase the specific load of contacts due to heat dissipation (chip resistors and resistant heating elements).

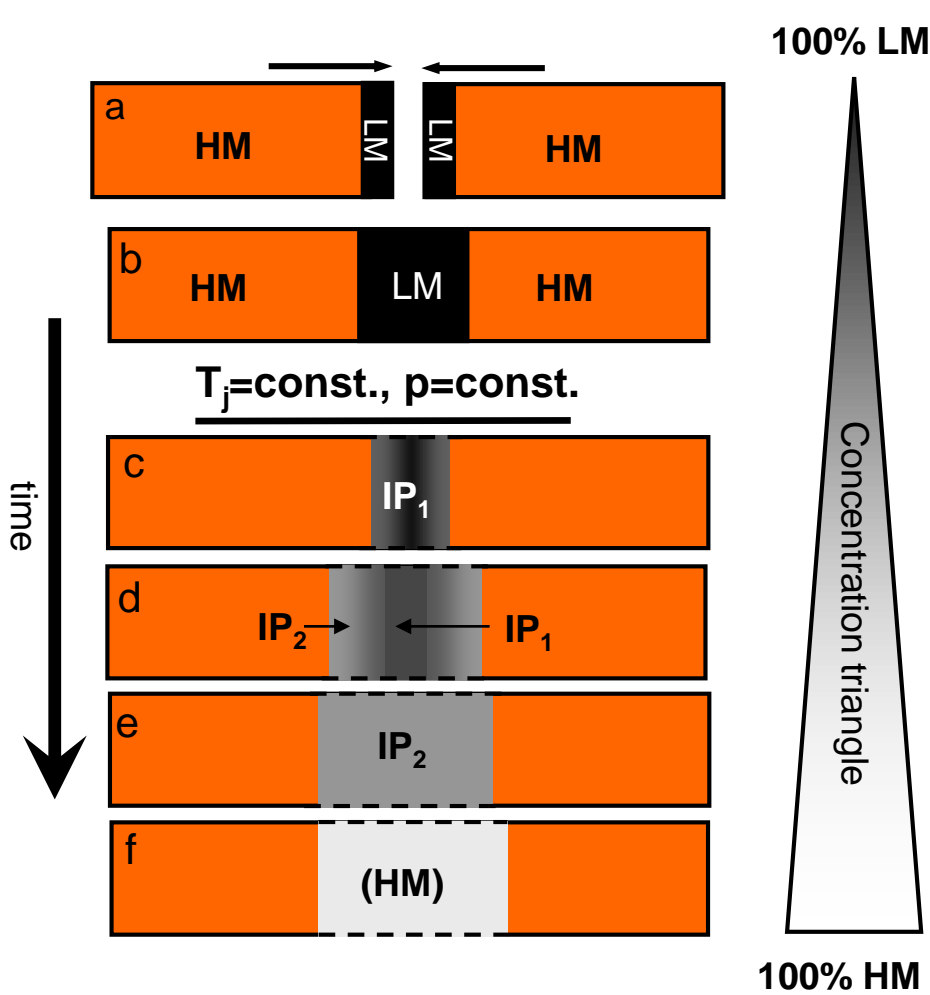
IBM: First a solution of the interconnection problem;  
Then everything else

# Need for a new lead-free joining technology

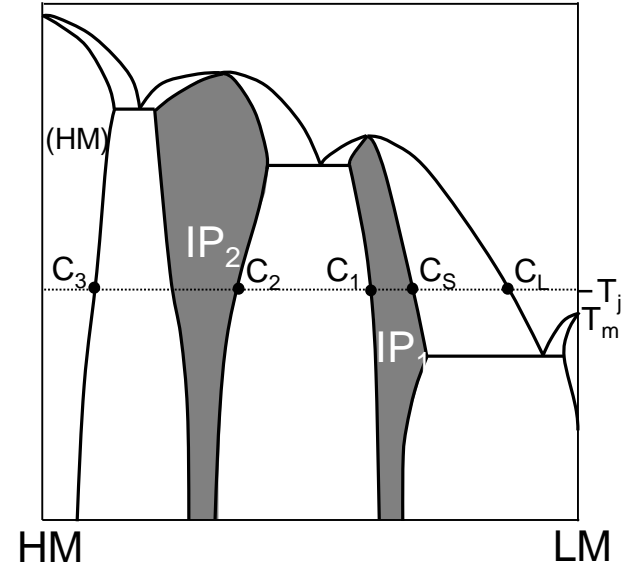
- \* High temperature stability:  
~2 to 3 fold higher than the fabrication temperature
- \* High mechanical strength
- \* Small energy consumption due to low fabrication temperature
- \* Environmentally friendly
- \* Reduction in size and cost
- \* No high temperature exposure of other circuit elements

- \* High temperature stable contacts of resistive heaters for thick-film hot plates, resistive elements for gas sensors
- \* Selective-area sealing and complete package sealing of microcircuits
- \* Development of packages for optoelectronics and other microcircuit application
- \* Wafer-to-wafer joining, flip-chip joining, 3D integration etc.
- \* Producing metal-polyethylene interconnection

# Diffusion Soldering



**HM-** high-melting component (**substrate**)  
**LM-** low-melting component (**solder**)  
**IP-** intermetallic phase  
**(HM)-** solid solution of LM in HM  
 $T_j$ - joining temperature  
 $T_m$ - melting temperature of LM



The *diffusion soldering* should be distinguished from the *diffusion brazing*. Although both processes involve the same bonding mechanism, the solid solution of LM in HM is ultimately formed during *brazing* in the interconnection area instead of the intermetallic phase. It also means that process is performed at such temperatures or for such systems where there is no intermetallic phases.

The term *transient liquid phase diffusion bonding* (TLP) is also frequently used to describe the joining process involving isothermal solidification. However, it is also claimed, that no interface remains after the TLP bonding operation, which resembles the diffusion brazing rather than diffusion soldering.

# Diffusion Soldering

## Conventional soldering

good joint filling  
tolerance to surface preparation

## Diffusion bonding

higher service temperature  
smaller thermal expansion mismatch stresses



# Potential candidates for diffusion soldering

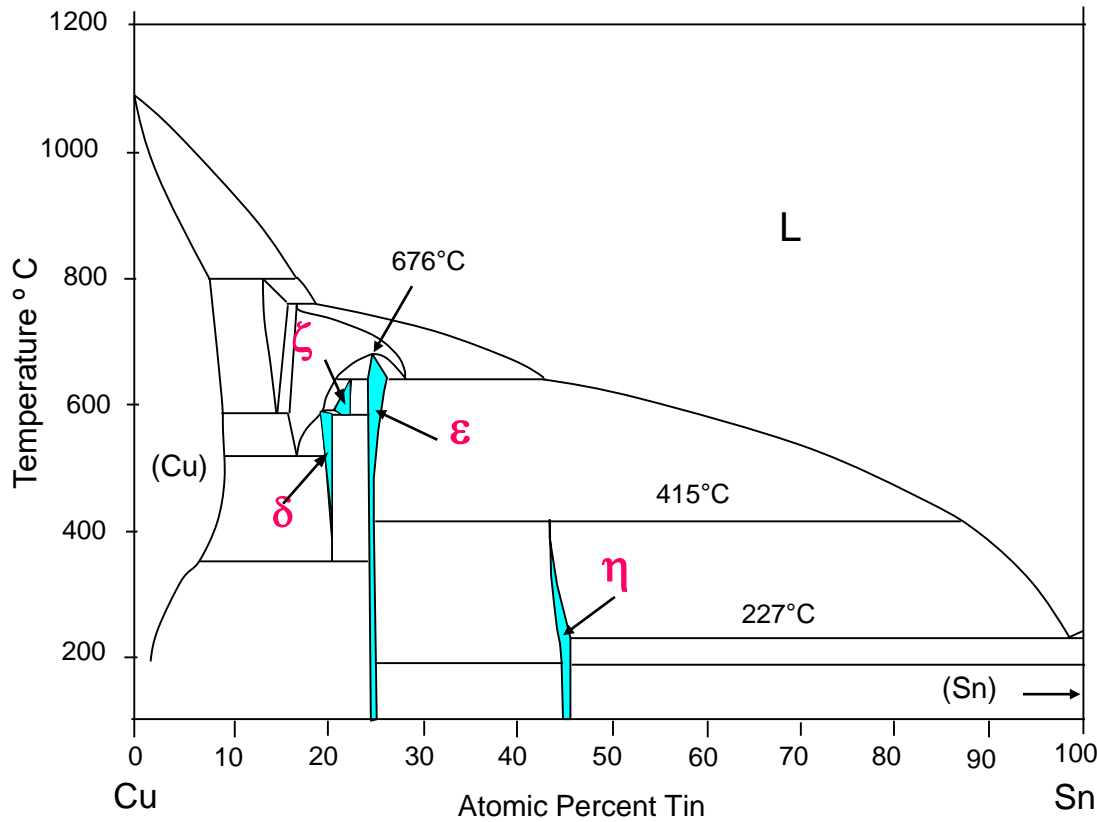
## solder's materials

interlayer  $\longrightarrow$  substrate

In-22 at.%Bi  $\longrightarrow$  Cu  
 In  $\longrightarrow$  Cu, Ag  
 In-48 at.%Sn  $\longrightarrow$  Cu  
 Sn  $\longrightarrow$  Ni, Cu, Ag  
 Al  $\longrightarrow$  Ni

No.	Element (at.%)			T <sub>M</sub> [°C]
	A	B	C	
1	In(60.3)	Bi(21.4)	Sn(18.3)	61
2	In(78)	Bi(22)	---	72
3	In(52)	Sn(48)	---	118
4	In	---	---	156
5	Sn(84.8)	Zn(15.2)	---	199
6	Sn	---	---	232
7	Al	---	---	660

# Cu/Sn/Cu interconnection



$\eta(\text{Cu}_6\text{Sn}_5)$ -hexagonal – 415 °C

$\epsilon(\text{Cu}_3\text{Sn})$ -orthorombic - 676 °C

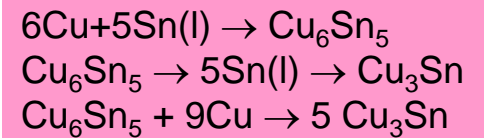
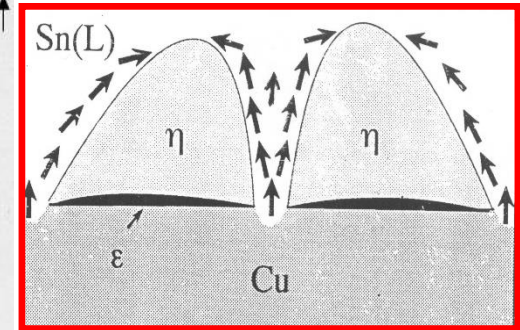
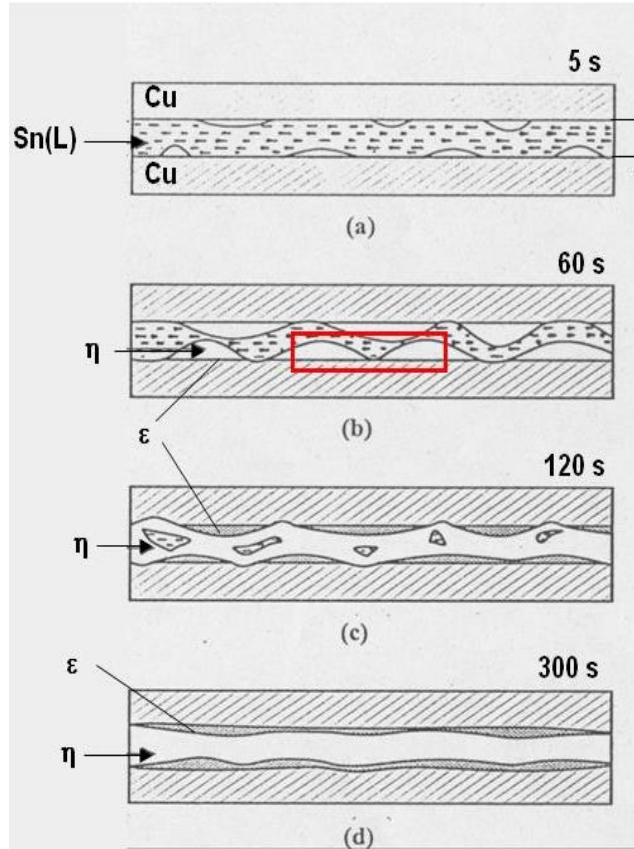
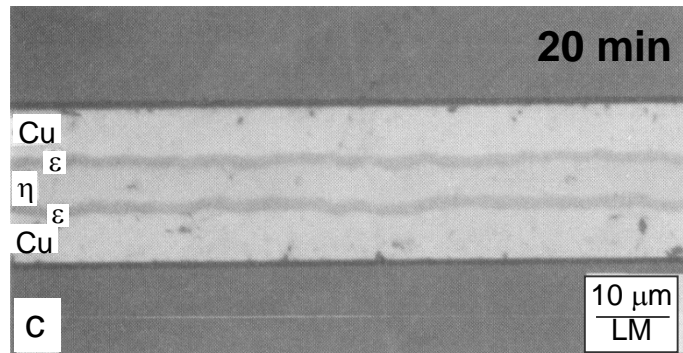
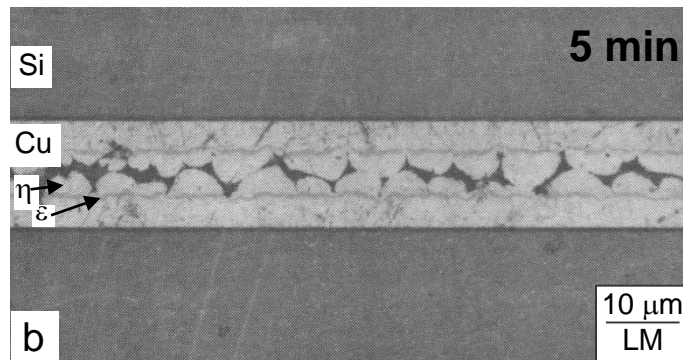
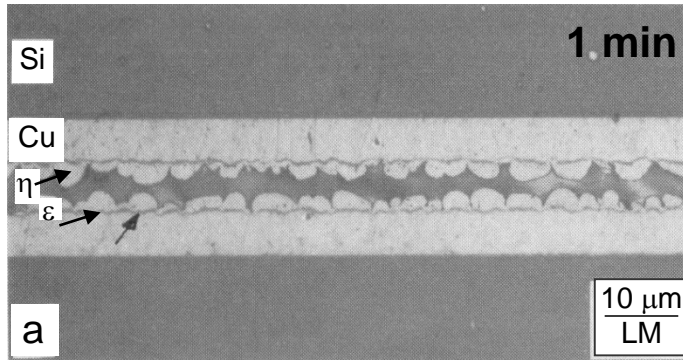
$\zeta(\text{Cu}_{10}\text{Sn}_3)$ -hexagonal – 642 °C

$\delta(\text{Cu}_{41}\text{Sn}_{11})$ -cubic – 586 °C

# Cu/Sn/Cu interconnection

240 °C

eutectic Pb-Sn- 183 °C



S. Bader, W. Gust, H. Hieber, *Acta Metall. Mater.* **43**, 329 (1995)

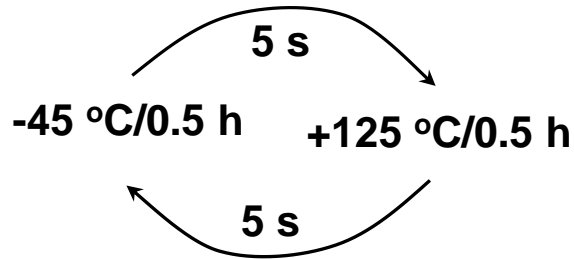
# Cu/Sn/Cu interconnection

## Tensile test

When both IPs present → strength twice larger than for pure Sn (18 MPa)

**DEMAND FOR ELECTRONIC INDUSTRY:  $R_m = 3-5 \text{ MPa}$**

## Thermal cycling



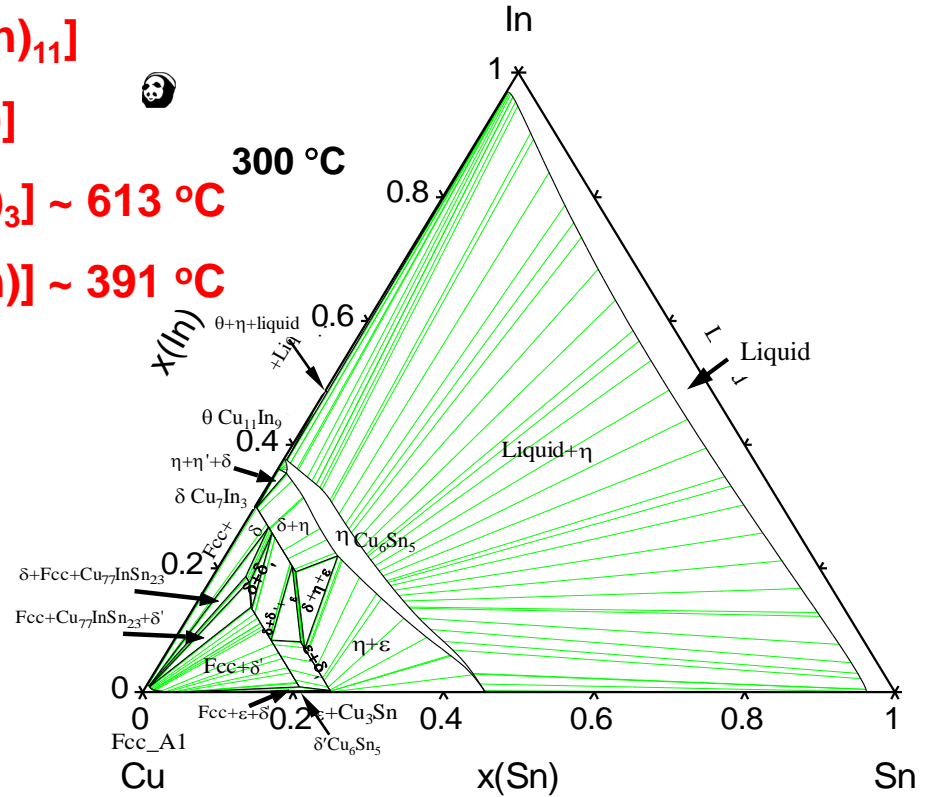
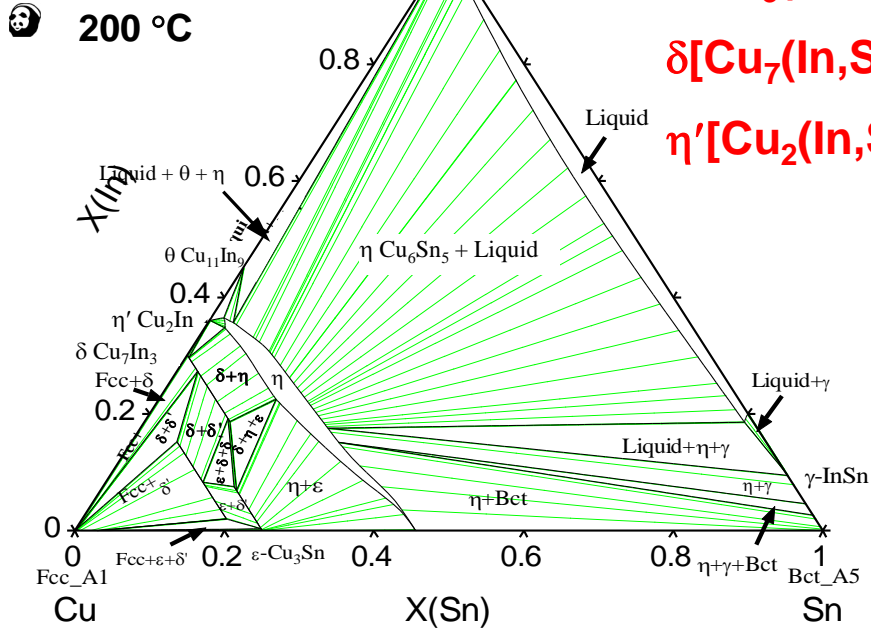
Cu/1.5  $\mu\text{m}$  Sn/Cu – 5 min at 330 °C

For more than 300 cycles and  $p=0.8 \text{ MPa}$

**$R_m = 27 \text{ MPa}$**

*S. Bader, W. Gust, H. Hieber, Acta Metall. Mater. 43, 329 (1995)*

# Cu/In-Sn/Cu interconnection



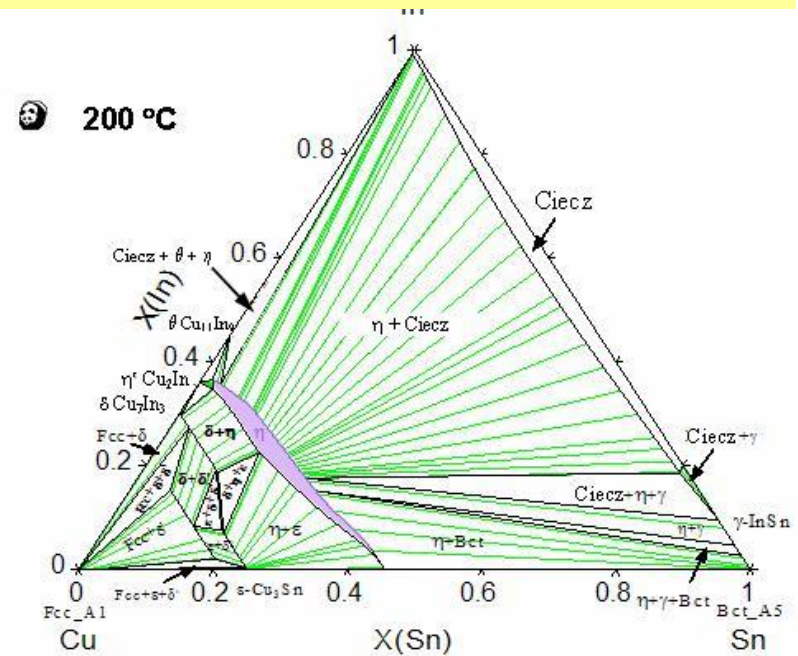
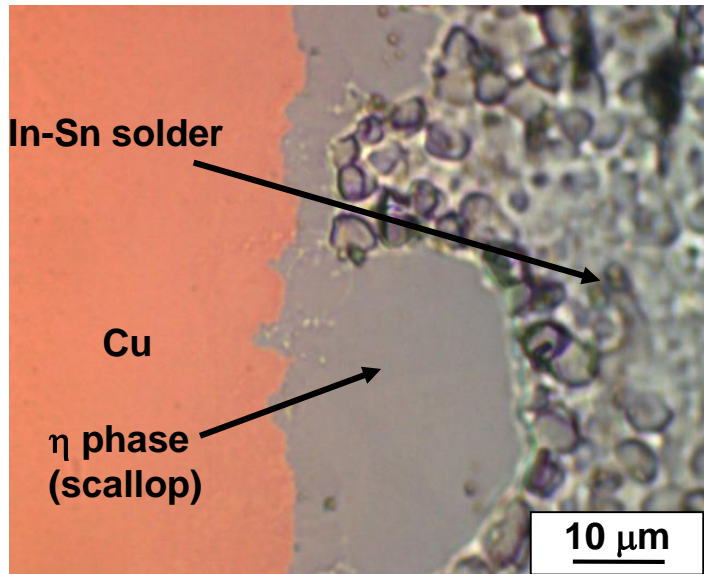
X.J. Liu, H.S.Liu, I. Ohnuma et. al.

*Experimental determination and thermodynamic calculation of the phase equilibria in the Cu-In-Sn system*

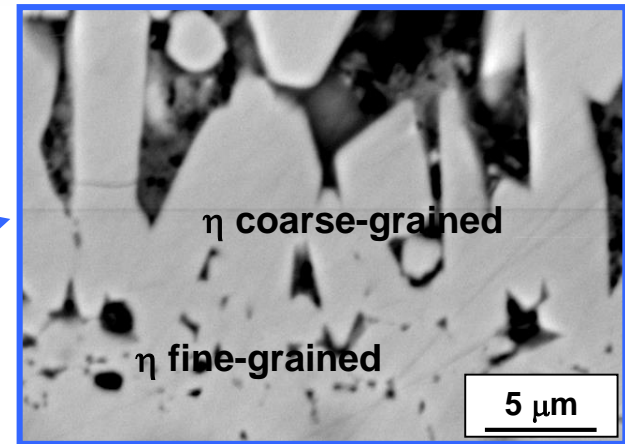
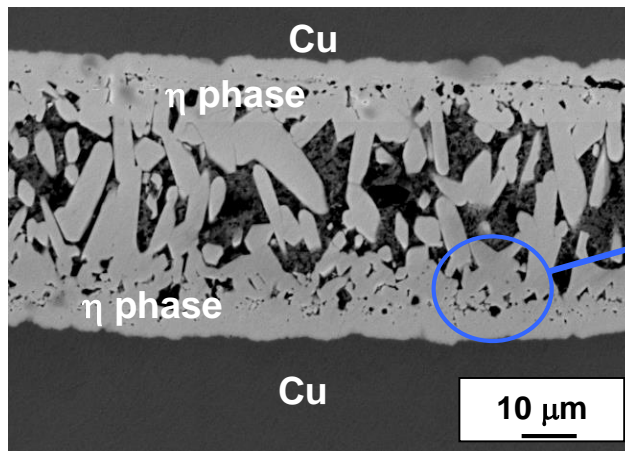
J. Electronic Mater. 30 (2001) 1093-1103

# Cu/In-48Sn/Cu interconnection - $\eta$ [Cu<sub>6</sub>(Sn,In)<sub>5</sub>] phase

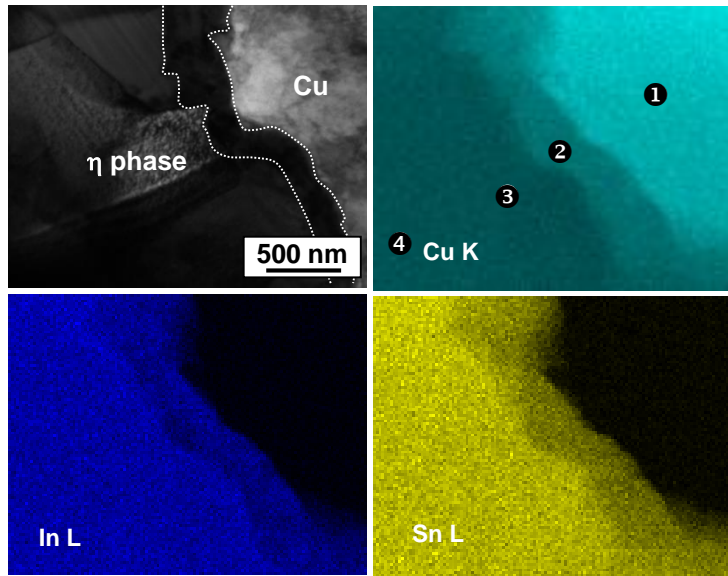
180 °C / 3 h



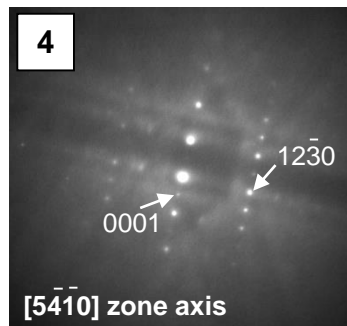
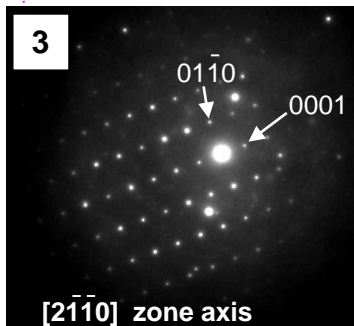
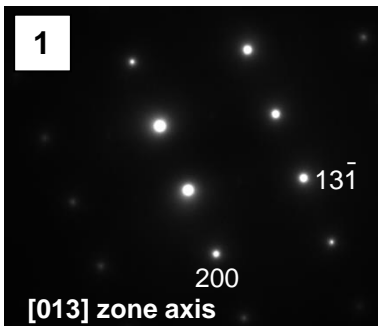
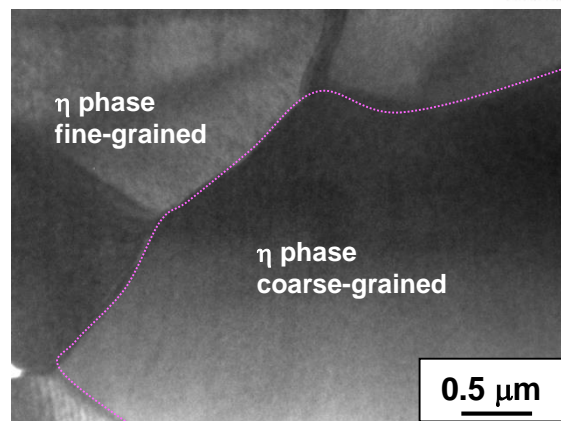
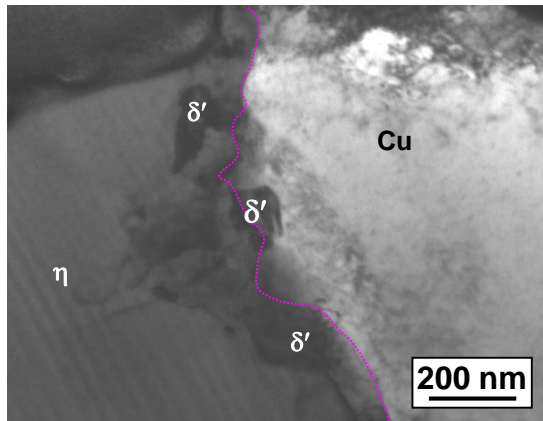
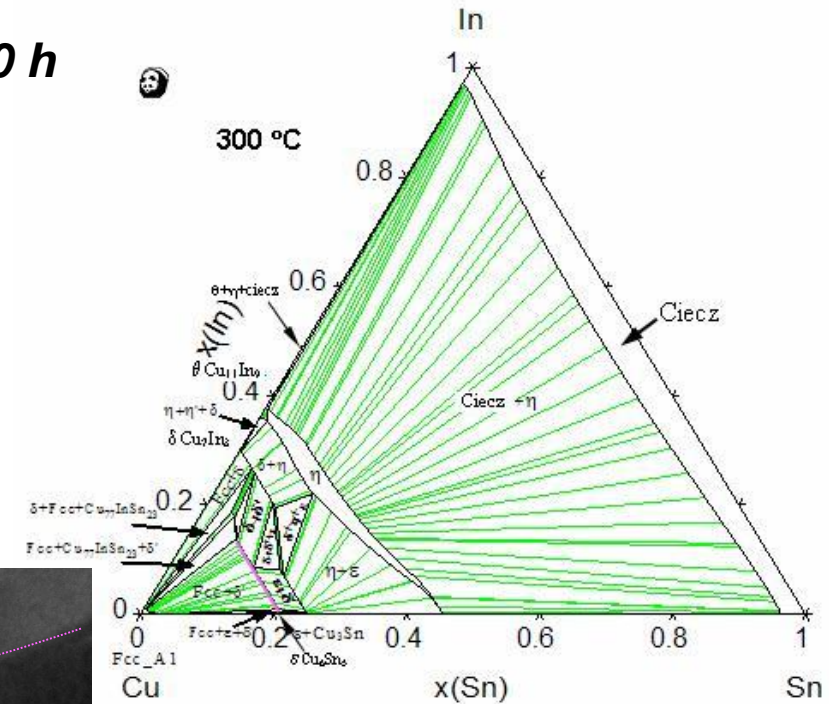
200 °C / 10 h



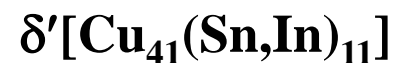
# Cu/In-48Sn/Cu interconnection- $\eta$ [Cu<sub>6</sub>(Sn,In)<sub>5</sub>] and $\delta'$ [Cu<sub>41</sub>(Sn,In)<sub>11</sub>] phases



200 °C / 60 h



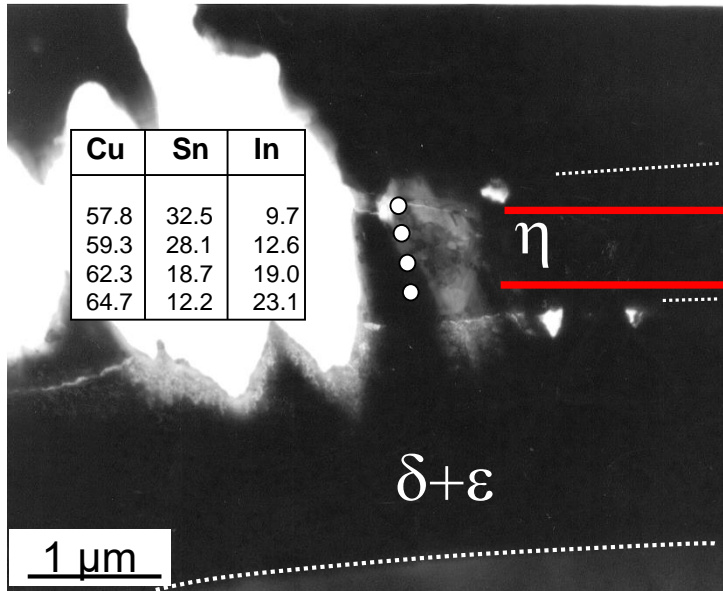
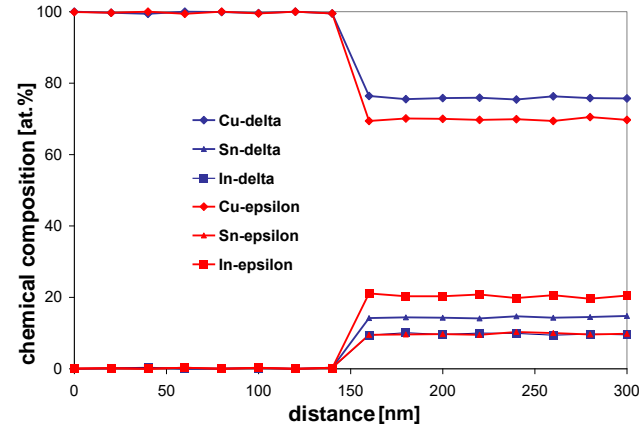
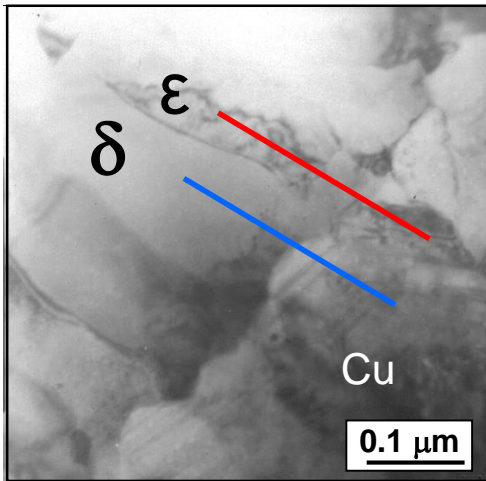
Lp.	Cu	In	Sn	phase
1	99.4	0.2	0.4	<i>Cu</i>
2	71.6	16.6	11.8	<i>δ'</i>
3	53.4	21.4	25.3	<i>η (fine)</i>
4	55.8	19.7	24.6	<i>η (coarse)</i>



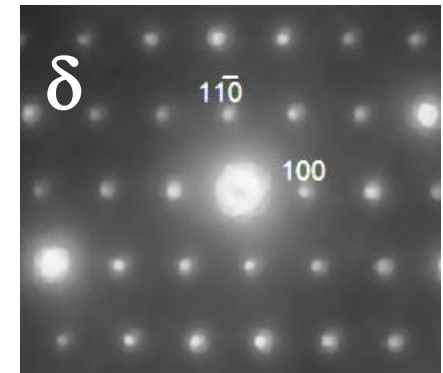
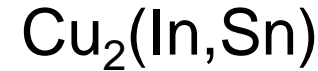




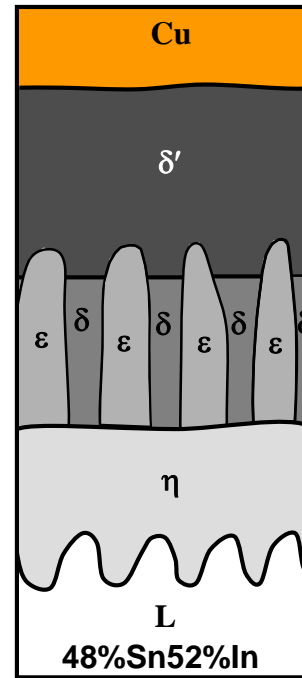
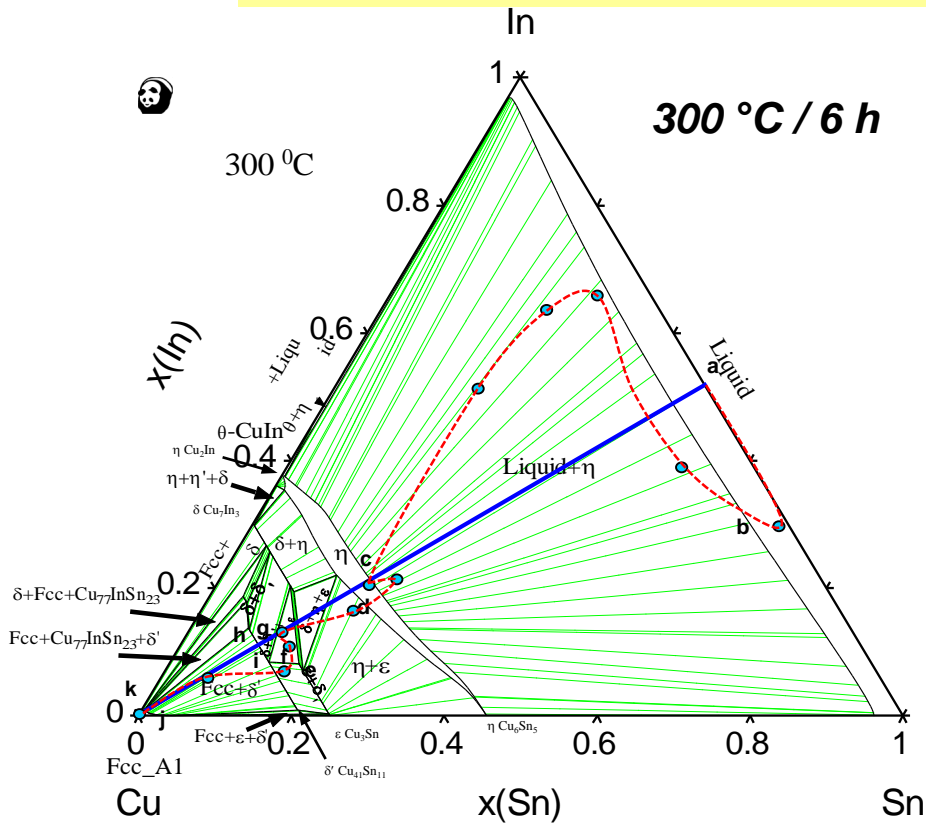
# Cu/In-48Sn/Cu interconnection - $\epsilon$ [Cu<sub>3</sub>(Sn,In)] and $\delta$ [Cu<sub>7</sub>(In,Sn)<sub>3</sub>] phases



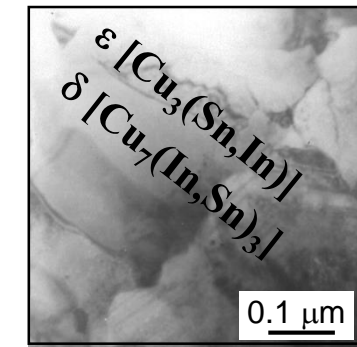
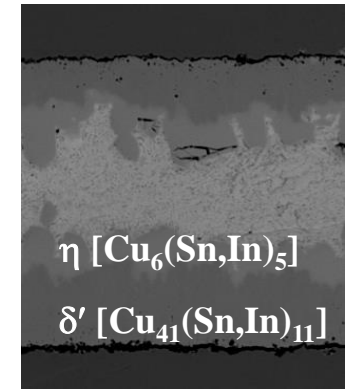
**220 °C / 243 h**



# Cu/In-48Sn/Cu interconnection - diffusion path



k  
i-j  
h  
f-g  
d-e  
c  
b  
a

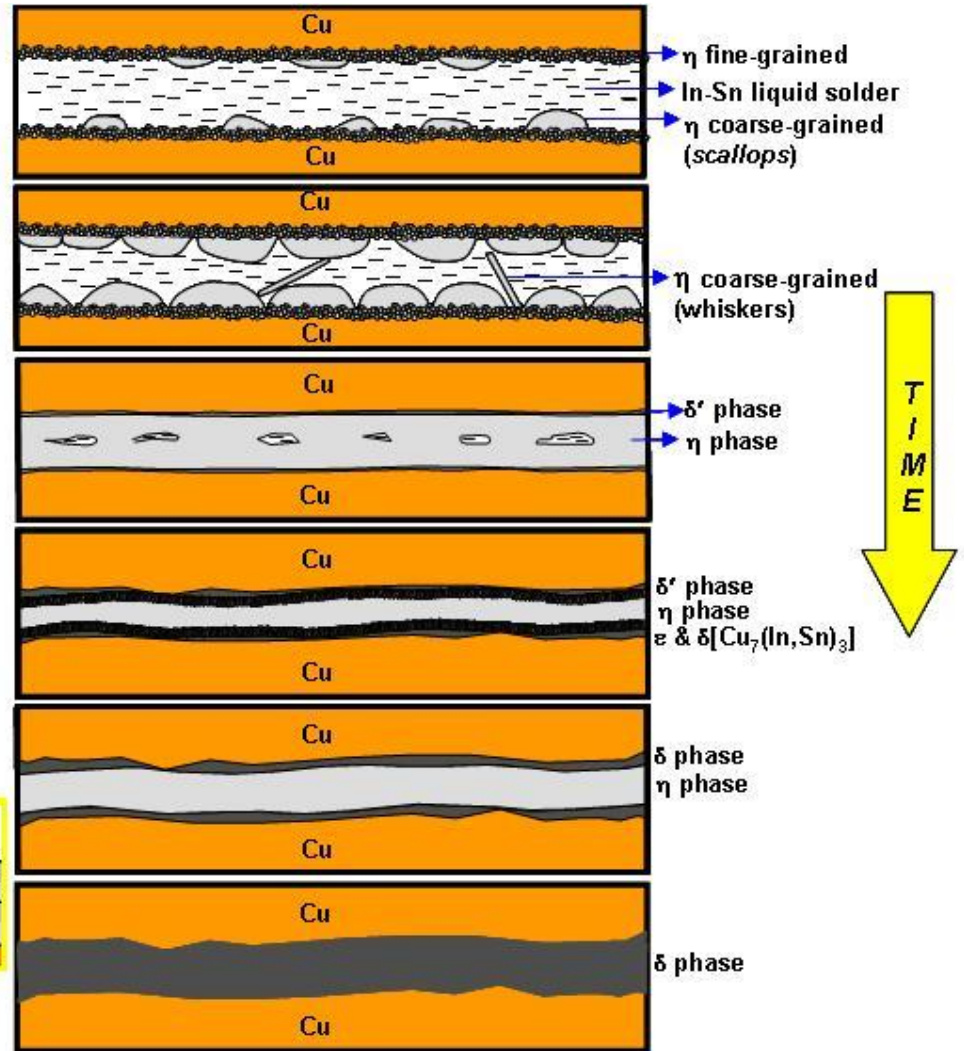
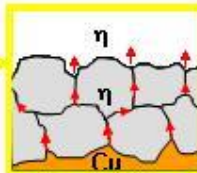
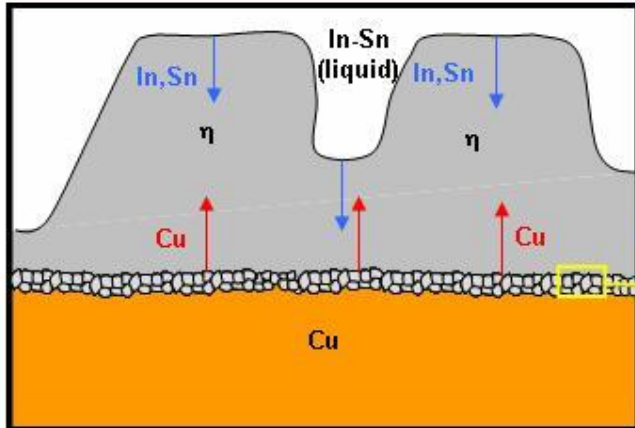
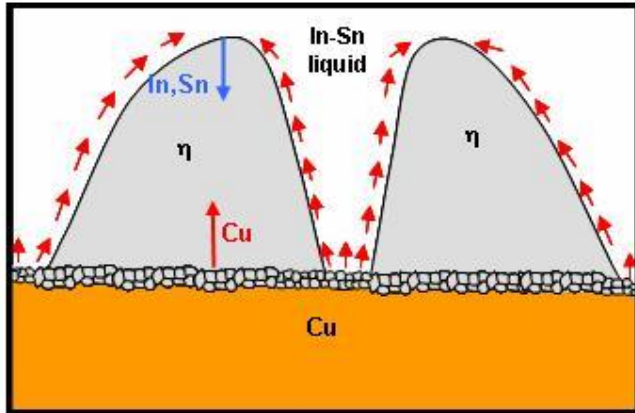


Concentration of elements at points 1-13 across the joint [at.%], SEM (EDX)													
	1	2	3	4	5	6	7	8	9	10	11	12	13
Cu	2	8	7	15	31	60	56	66	74	74	76	91	100
In	32	39	64	62	51	20	22	17	13	10	6	5	0
Sn	66	53	29	23	18	20	22	17	13	16	18	4	0

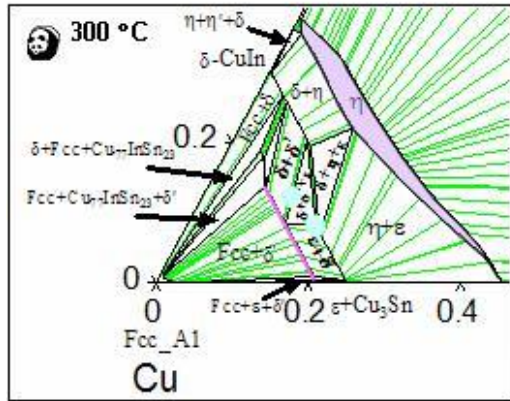
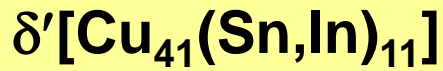
Clark J.B.: Conventions for plotting the diffusion paths in multiphase ternary diffusion couples on the isothermal section of a ternary phase diagram, Trans. Metal. Soc. AIME 227, (1963) 1250-1251.

# $\eta[\text{Cu}_6(\text{Sn},\text{In})_5]$

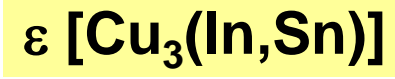
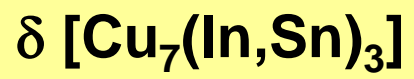
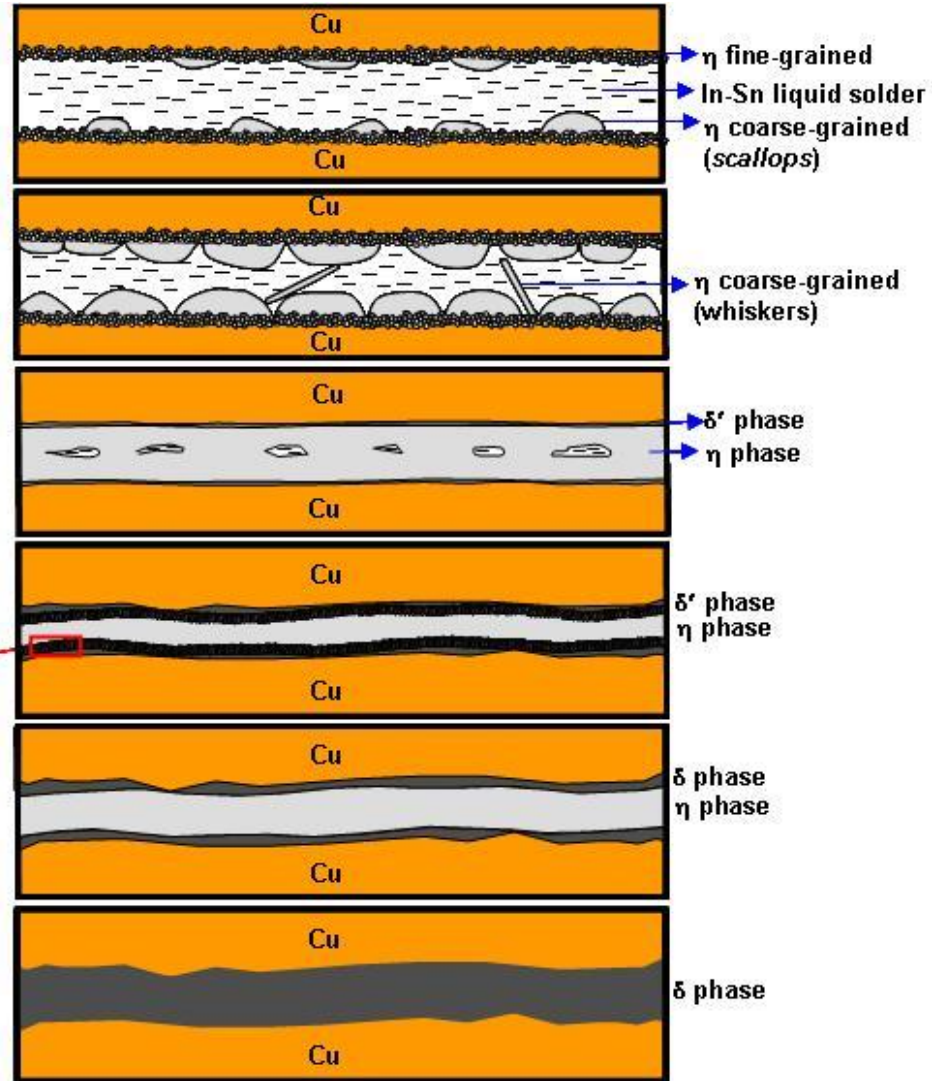
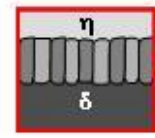
180-220 °C



300 - 350 °C



TIME



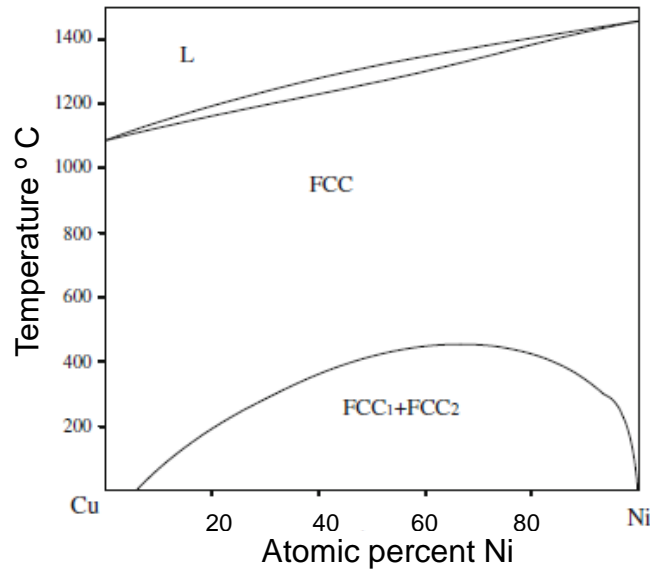
## Shear test at room temperature in Cu/In-48Sn/Cu joint

Temperature and time of production	Phases present in the joined area	Shear strength [MPa]
200 °C/3 hours	$\eta[\text{Cu}_6(\text{Sn},\text{In})_5]$ and In-Sn solder	4.6
200 °C/5 hours	$\eta[\text{Cu}_6(\text{Sn},\text{In})_5]$ and In-Sn solder	7.1
200 °C/3 days	$\eta[\text{Cu}_6(\text{Sn},\text{In})_5]$	11.2
250 °C/3 hours	$\eta[\text{Cu}_6(\text{Sn},\text{In})_5]$ , $\delta'[\text{Cu}_{41}(\text{Sn},\text{In})_{11}]$ , In-Sn solder	4.2
300 °C/3 hours	$\eta[\text{Cu}_6(\text{Sn},\text{In})_5]$ , $\delta'[\text{Cu}_{41}(\text{Sn},\text{In})_{11}]$ , In-Sn solder	4.7
300 °C/5 hours	$\eta[\text{Cu}_6(\text{Sn},\text{In})_5]$ , $\delta'[\text{Cu}_{41}(\text{Sn},\text{In})_{11}]$ , In-Sn solder	6.4
300 °C/3 days	$\delta'[\text{Cu}_{41}(\text{Sn},\text{In})_{11}]$	28.5

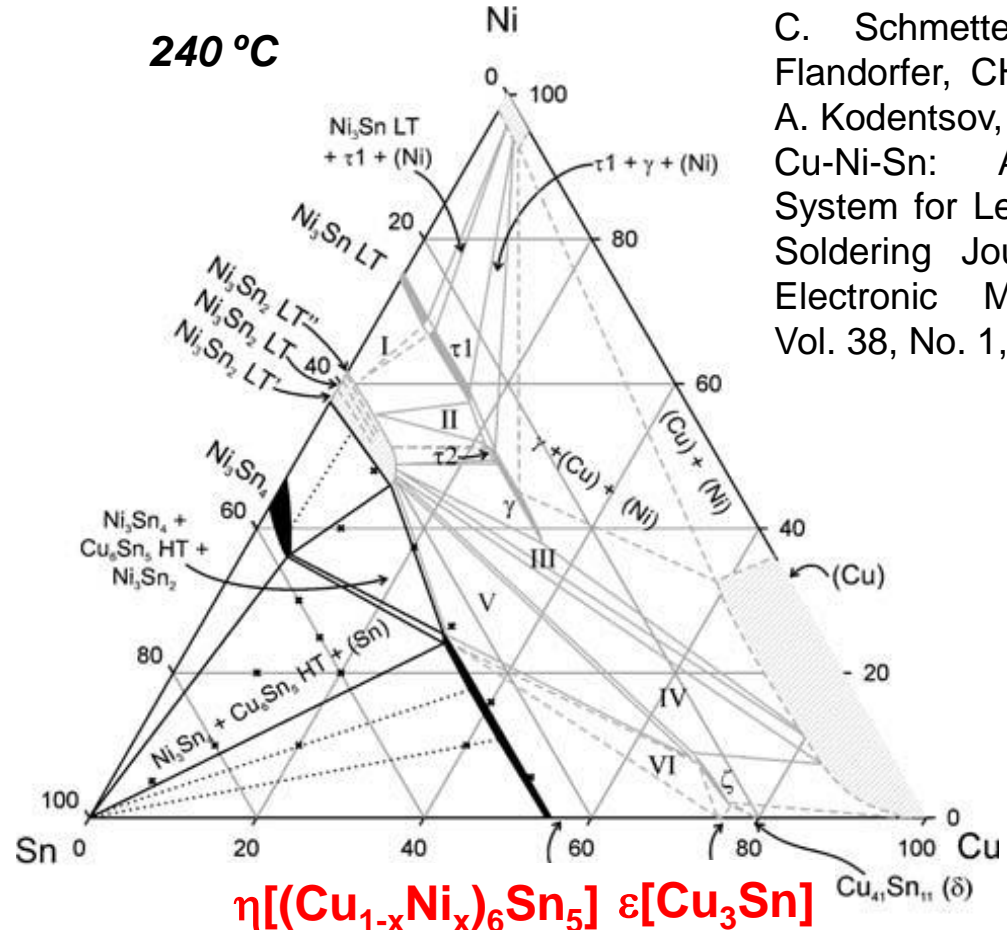
## Shear test at elevated temperature in Cu/In-48Sn/Cu joint

Temperature and time of production	Phases present in the joined area	Shear strength [MPa]
200 °C/3 days	$\eta[\text{Cu}_6(\text{Sn},\text{In})_5]$	9.5 (test at 100 °C)
200 °C/2 weeks	$\eta[\text{Cu}_6(\text{Sn},\text{In})_5]$ and $\delta'[\text{Cu}_{41}(\text{Sn},\text{In})_{11}]$	9.8 (test at 100 °C)
300 °C/1 week	$\delta'[\text{Cu}_{41}(\text{Sn},\text{In})_{11}]$	The fracture occurred in copper substrate. Joint was not destroyed at 100 and 150 °C
300 °C/2 weeks	$\delta'[\text{Cu}_{41}(\text{Sn},\text{In})_{11}]$	The fracture occurred in copper substrate. Joint was not destroyed at 100 and 150 °C

# Cu+5at.%Ni/Sn/Cu+5at.%Ni interconnection



Cuiping Wang, Jinjin Zhu, Yong Lu, Yihui Guo, and Xingjun Liu, Journal of Phase Equilibria and Diffusion Vol. 35 No. 1, 2014.

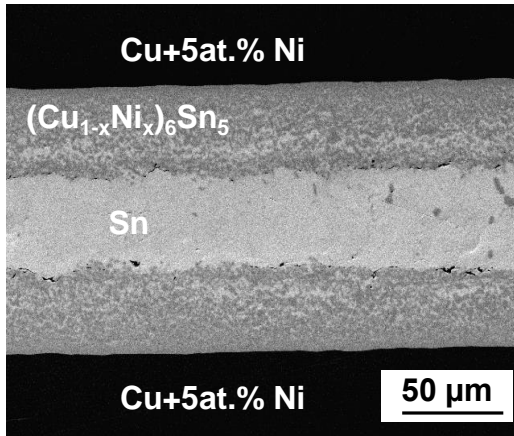


C. Schmetterer, H. Flandorfer, CH. LueF, A. Kodentsov, H. Ipser Cu-Ni-Sn: A Key System for Lead-Free Soldering Journal of Electronic Materials, Vol. 38, No. 1, 2009.

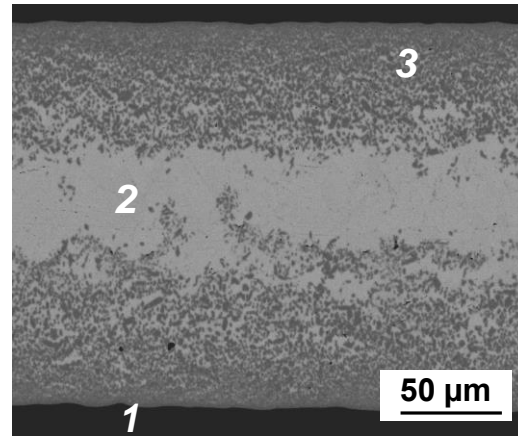


# Cu+5at.%Ni/Sn/Cu+5at.%Ni interconnection $\eta[(\text{Cu}_{1-x}\text{Ni}_x)_6\text{Sn}_5]$

250°C / 2 min

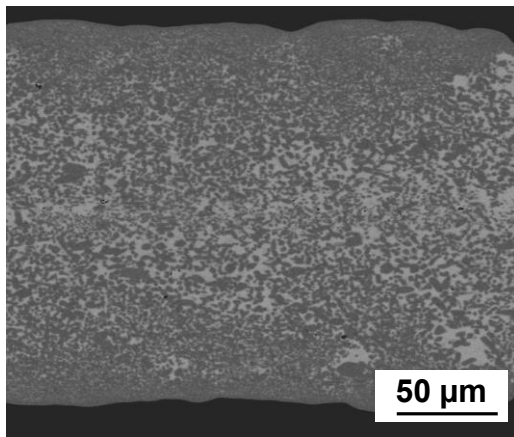


250°C / 5 min

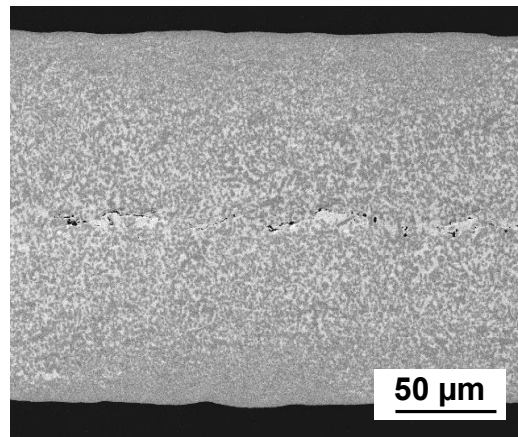


Lp.	Cu at.%	Sn at.%	Ni at.%	phase
1	95.1	0.0	4.9	(Cu,Ni)
2	0.0	100.0	0.0	Sn
3	50.7	45.1	4.2	$\eta$

250°C / 10 min

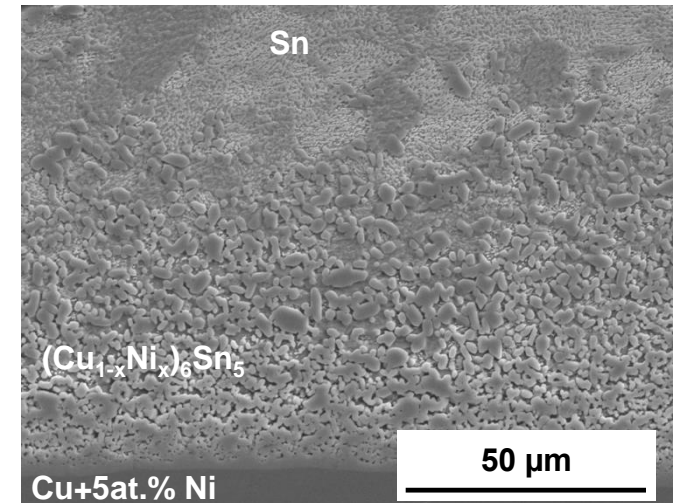


250°C / 15 min



260°C / 10 min

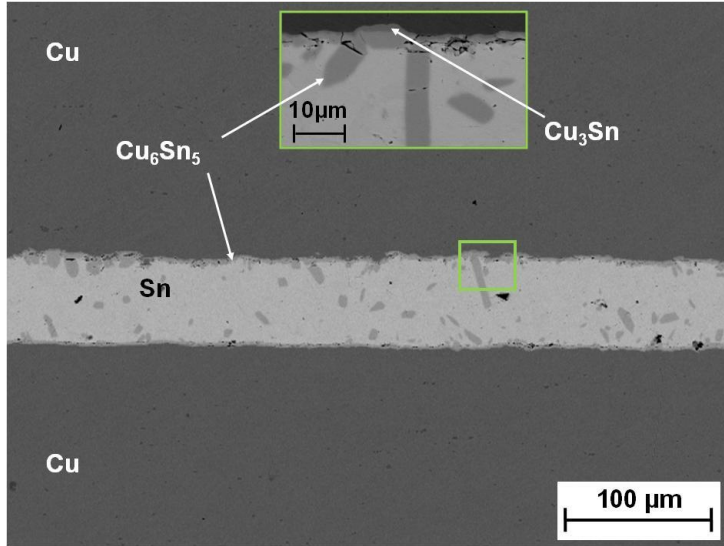
after selectively etching away the Sn



**$\epsilon[\text{Cu}_3\text{Sn}]$  WAS NOT OBSERVED**

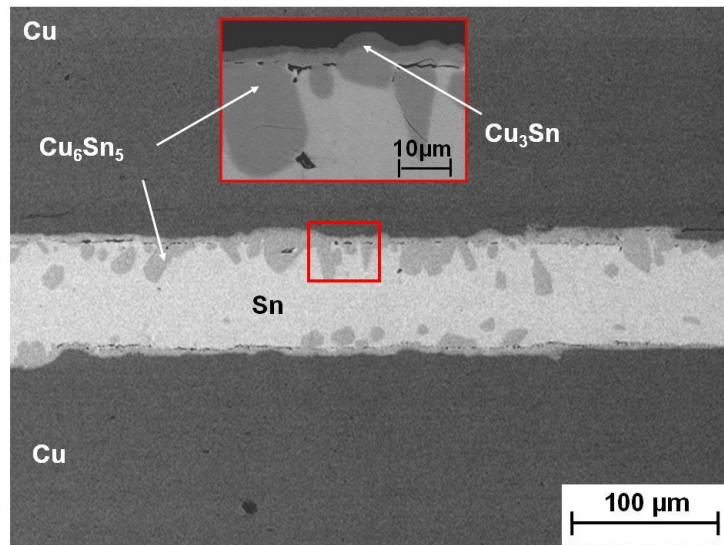
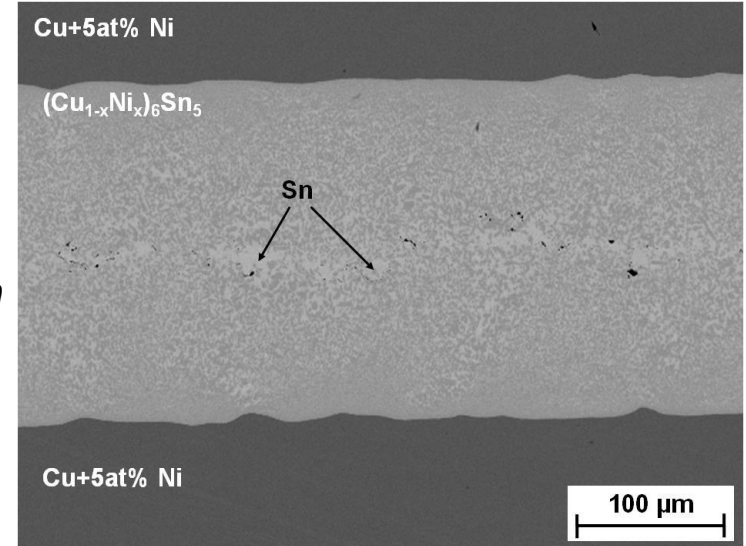
# Cu/Sn/Cu vs. Cu+5at.%Ni/Sn/Cu+5at.%Ni joints

## Cu/Sn/Cu

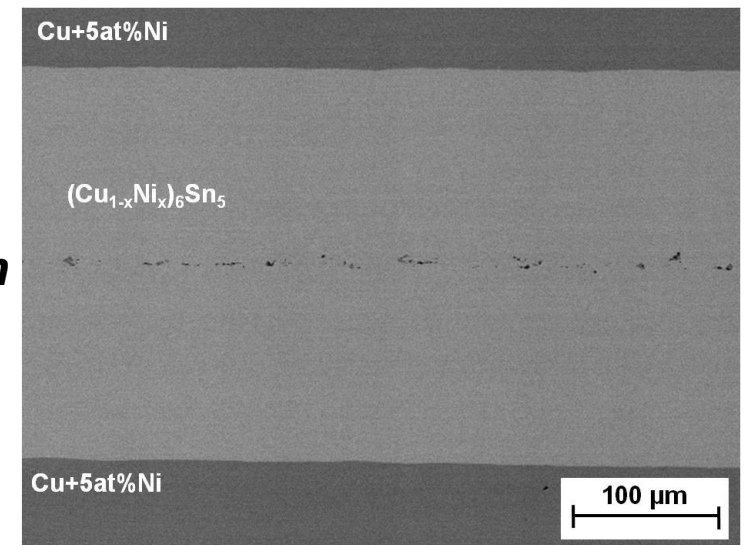


250°C / 10 min

## Cu+5at.%Ni/Sn/Cu+5at.%Ni



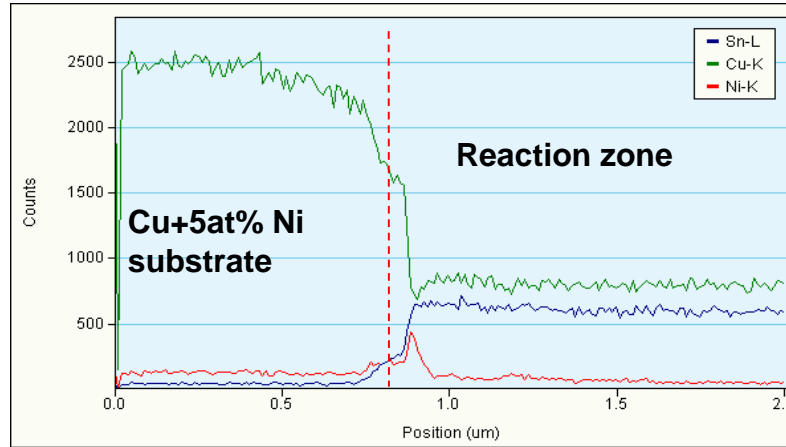
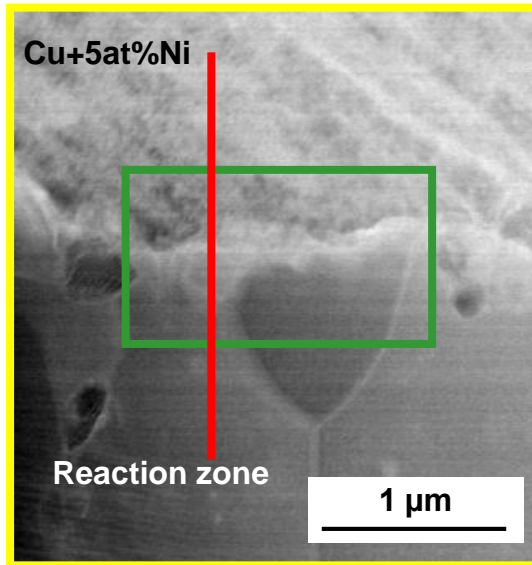
250°C / 60 min



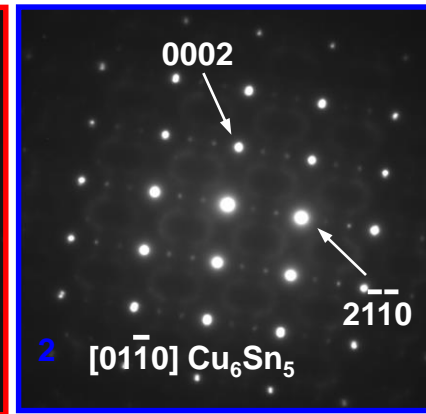
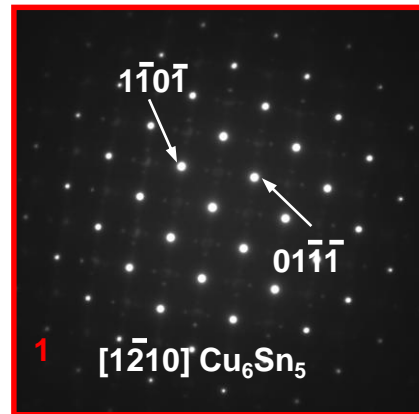
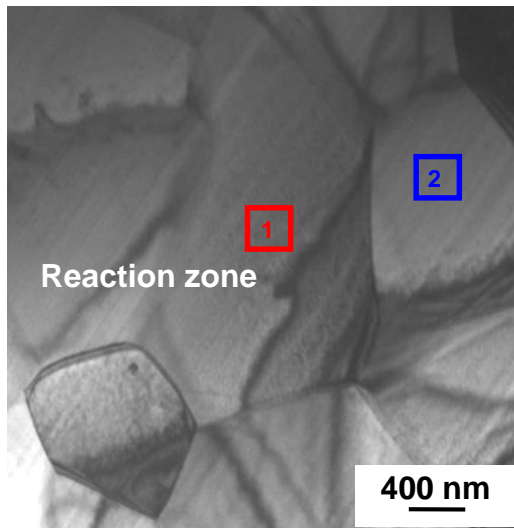


# Cu+5at.%Ni/Sn/Cu+5at.%Ni interconnection $\eta[(Cu_{1-x}Ni_x)_6Sn_5]$

250 °C / 60 min



*Ni enrichment zone-possible reason that no  $\epsilon[Cu_3Sn]$  formed*



KPMI Fall Meeting, 13-14 November 2014, Yeosu, South Korea

Paweł Zięba, Anna Wierzbicka-Miernik

Diffusion soldering- fundamentals and application

# Rate controlling factor of IPs growth

in general:

$$x=kt^n$$

the width of the  
phase interlayer

time

Growth rate constant

\* for **n=0.5**: parabolic growth,  
controlled by the volume diffusion

\* for **n=1**: linear growth,  
controlled by the chemical  
reaction at the phase boundary

There are two important advantages of linear growth from the diffusion soldering process point of view:

1. faster formation of the intermetallic phase,
2. shorter time needed to create a joint.

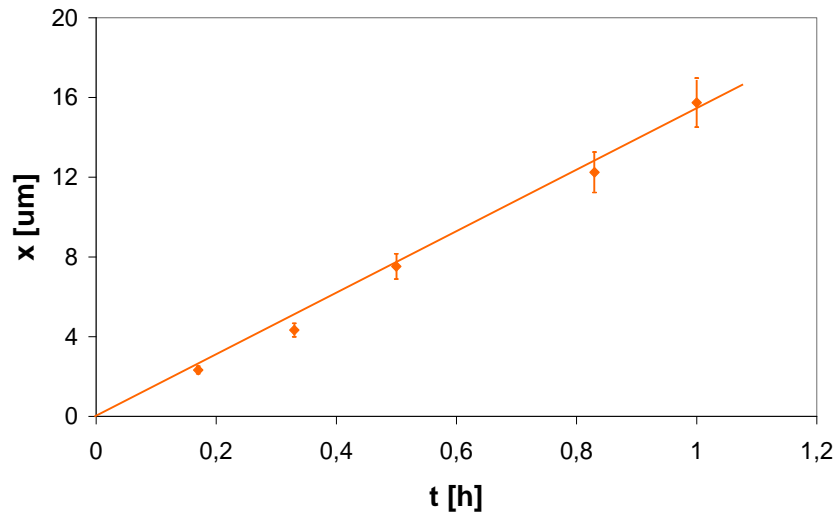
# Growth kinetics $\eta$ and $\varepsilon$ phases in Cu/Sn/Cu joint

<b><math>\eta</math> phase</b>	<b><math>n</math></b>
240 °C	0.21
300 °C	0.26
<b><math>\varepsilon</math> phase</b>	<b><math>n</math></b>
240 °C	0.45
300 °C	0.49

*S. Bader, W. Gust, H. Hieber, Acta Metall. Mater.* **43**, 329 (1995)

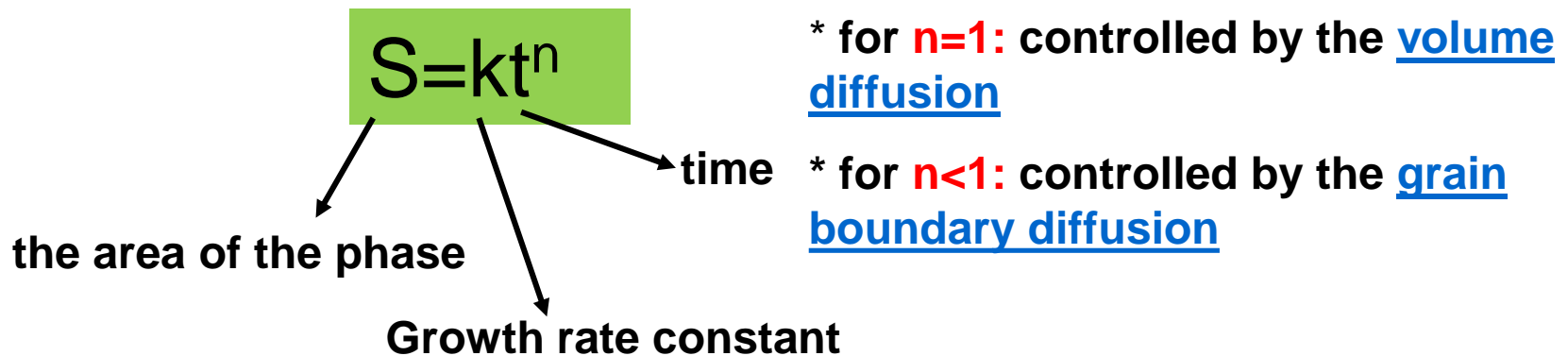
# Growth kinetics $\eta$ and $\delta'$ phases in Cu/In-48Sn/Cu joint

## $\delta'$ at 300 °C



$\eta$ phase	$n$
180 °C	$0.40 \pm 0.02$
200 °C	$0.20 \pm 0.03$
220 °C	$0.31 \pm 0.09$
$\delta'$ phase	$n$
300 °C	$1.08 \pm 0.04$
325 °C	$1.10 \pm 0.08$
350 °C	$0.91 \pm 0.04$

# Rate controlling factor of IPs growth in Cu+5at.%Ni/Sn/Cu+5at.%Ni joints

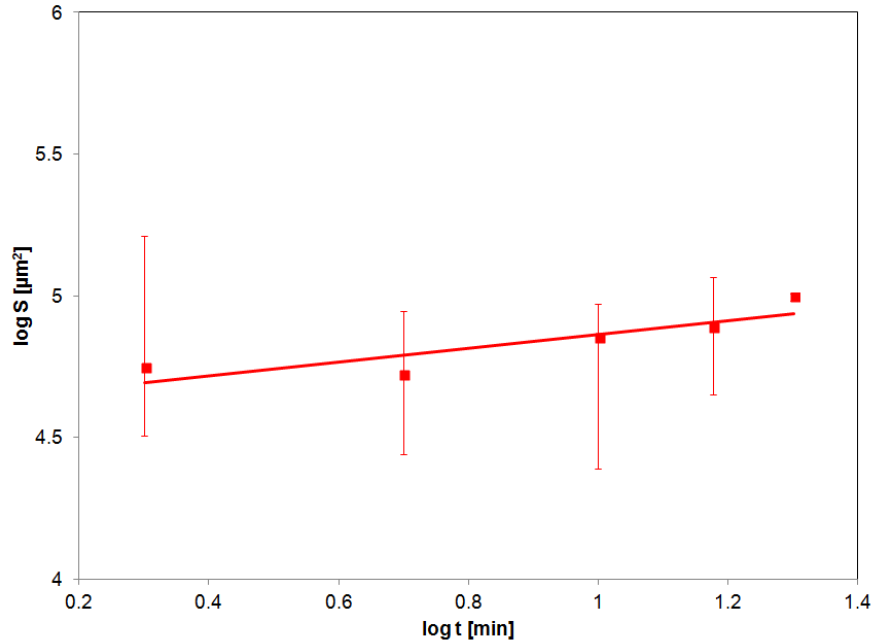


Wierzbicka-Miernik A., Miernik K., Wojewoda-Budka J., Filipek R., Lityńska-Dobrzyńska L., Kodentsov A., Zieba P. (2013): *Growth kinetics of the intermetallic phase in diffusion-soldered Cu+5at.%Ni)/Sn/(Cu+5at.%Ni) interconnections*, Materials Chemistry and Physics 142, 682-685.



# Growth kinetics $\eta$ phase in Cu+5at.%Ni/Sn/Cu+5at.%Ni joint

$\eta$  at 250 °C



<i><math>\eta</math> phase</i>	<i>n coefficient</i>
240 °C	$0.27 \pm 0.08$
250 °C	$0.24 \pm 0.08$
260 °C	$0.15 \pm 0.09$

**$n < 1$ : grain boundary diffusion**

# Conclusions

The diffusion soldering is a novel interconnection technology. It is characterized by the low joining temperature (comparable with Pb-Sn eutectic solders) combined with the service temperature, which can be higher by several hundred degrees than the joining temperature. For these reasons, diffusion soldering is a very attractive method for certain types of electronic application.

The proces can be even more efficient if:

- the growth kinetics of IPs is controlled either by chemical reactions at the interfaces or grain boundary diffusion processes,
- The substrate is doped with other elements preventing growth of undesired IPs like  $\epsilon$ -Cu<sub>3</sub>Sn and accelerating growth kinetics due to change of formation mechanism of IPs (see  $(\text{Cu}_{1-x}\text{Ni}_x)_6\text{Sn}_5$ ).